

TO HOST

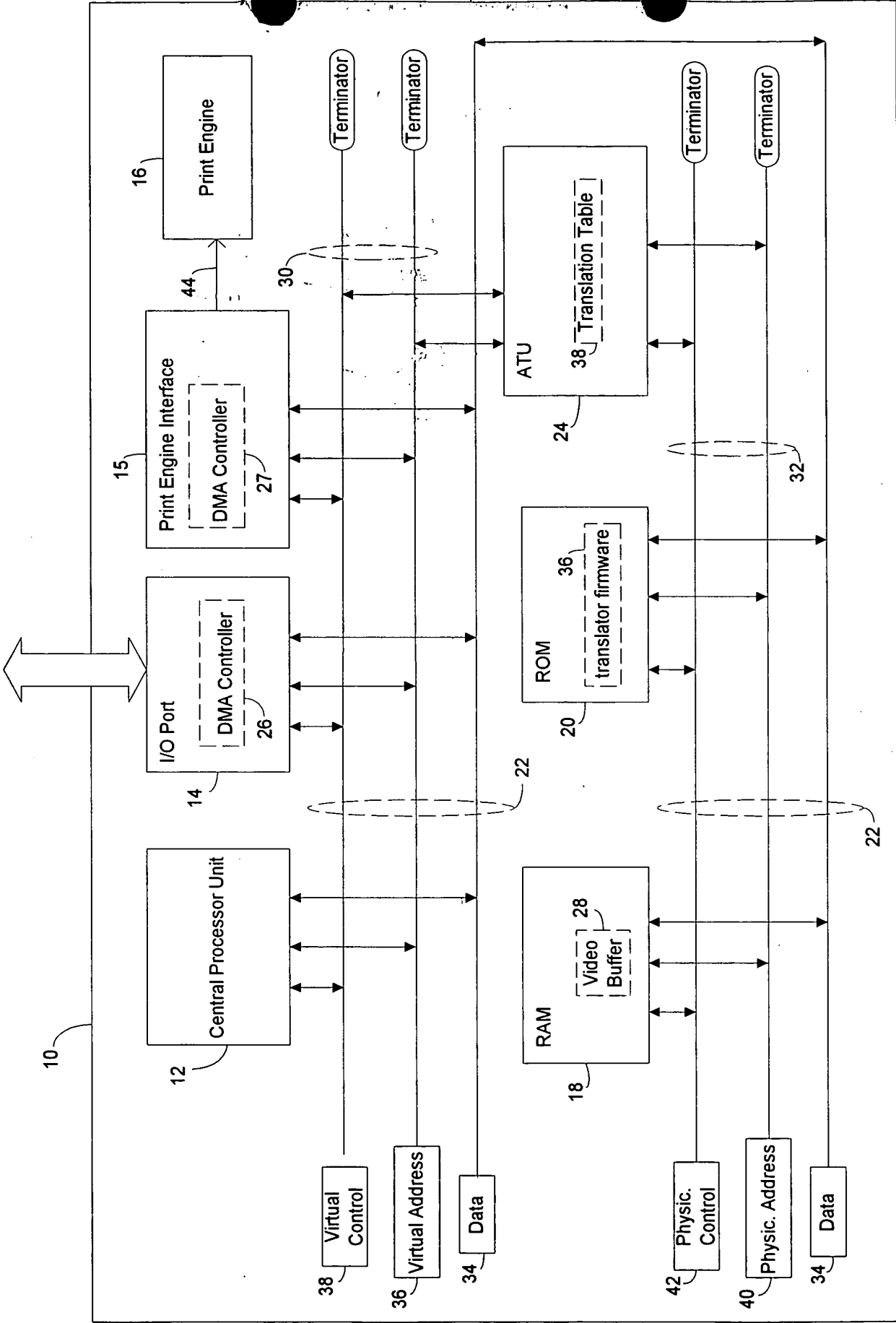


FIG. 1

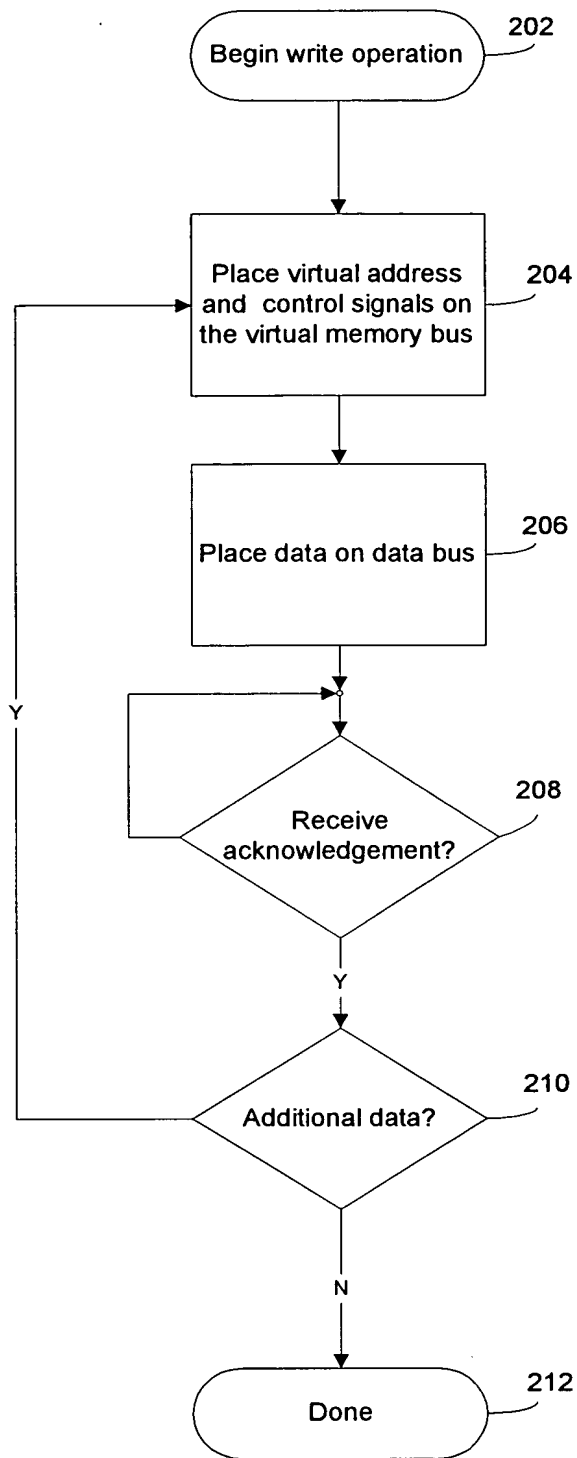


FIG. 2

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graph TD; 302([Receive virtual address from bus master over virtual memory bus]) --> 304[Translate virtual address into physical address using table]; 304 --> 306[Place physical address on the physical memory bus]; 306 --> 308[Generate and transmit acknowledgement]; 308 --> Done([Done]);
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302

Receive virtual address from bus master over virtual memory bus

304

Translate virtual address into physical address using table

306

Place physical address on the physical memory bus

308

Generate and transmit acknowledgement

Done

FIG. 3